Some common ARM assembly errors - and what they will look like

**missing symbol**

/tmp/ecdWtiwt.o: In function `main':
test.s:(.text+0x10): undefined reference to `loop'
collect2: ld returned 1 exit status

**multiply-defined symbol (note the line number in the second message)**

    test.s: Assembler messages:
    test.s:15: Fatal error: Symbol loop already defined.

**forgot to use # to designate a constant (note the line)**

    test.s: Assembler messages:
    test.s:12: Error: immediate expression requires a # prefix -- `mov r0,1'

**forgot to use `.section "".data" directive and then you store to data that is assembled into the default "".text" section**

    <no error message during assembly>
    during execution:
        qemu: uncaught target signal 4 (Illegal instruction) - core dumped
        Illegal instruction

**forgot to put the return address in the pc**

    <no error message during assembly>
    during execution:
        qemu: uncaught target signal 4 (Illegal instruction) - core dumped
        Illegal instruction